

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: **Hitoshi ASADA et al.**

Serial Number: **Not Yet Assigned**

Filed: **September 17, 2003**

**Customer No.: 23850**

For: **SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE  
SAME**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

September 17, 2003

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,  
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Enclosures: PTO-1449; References (4)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. 031168	Serial No. New Appln.
	Applicant(s): Hitoshi ASADA	
	Filing Date: September 17, 2003	Group Art Unit:

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AE 6-112219	04/22/94	Japan	Abstract.
_____ AF 9-260590	10/03/97	Japan	Abstract.
_____ AG 11-126900	05/11/99	Japan	Abstract.
_____ AH 11-195789	07/21/99	Japan	Abstract.
_____ AI			

### OTHER DOCUMENTS

_____	AJ			
_____	AK			
<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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